

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	191	(I/O) and (pin pad cell) and placement same current and "716"/\$.ccls. and @ad<"20030624"	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/12/16 14:23
S11	47	(I/O) and (pad cell) and system same (switch\$4 adj current) and placement	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/12/16 14:23
S12	2	(I/O) same (pad cell) same (system Group) same (switch\$4 adj current) same placement	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/06/13 17:09
S13	4	(I/O) same (pad cell) same (system Group) same (switch\$4 adj current)	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/06/13 17:10
S14	770	(I/O) same (pad cell) same (system Group) same (position\$4 location)	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/06/13 17:10
S15	24	(I/O) and (cell pad) same (group) same (chip layout) same placement same current and (switch\$4)	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/06/13 17:15
S16	3	(I/O) same (cell pad) same (group) same (chip layout) same placement same (switch\$4 current)	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/06/13 17:17
S17	2	(I/O) same (cell pad) same (group) same (chip layout) same placement same (switch\$4 current)same control\$4 same file	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/06/13 17:18
S18	1	(I/O) same (cell pad) same (set group) same (chip layout) same placement same (switch\$4 current)same control\$4 same file and "716"/\$.ccls.	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/06/13 17:19
S19	2	(I/O) same (cell pad) same (set group) same (chip layout) same placement same (switch\$4 current) and "716"/\$.ccls.	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/06/13 17:19
S20	4	(I/O) same (cell pad) same (set group) same (chip layout) same placement same (switch\$4 current)	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/06/13 17:26
S21	39	(I/O) and (cell pad) same (set group) same (chip layout) same placement same (switch\$4 current)	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/06/13 17:28

S22	3	(I/O adj pad) and (cell pad) same (set group) same (chip layout) same placement same (switch\$4 current)	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/06/13 17:29
S23	39	(I/O) and (cell pad) same (set group) same (chip layout) same placement same (switch\$4 current)	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/06/13 17:29
S24	22	(I/O) and (cell pad) same (set group) same (chip layout) same placement same (switch\$4 current) and "716"/\$.ccls.	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/06/13 17:36
S25	3	(I/O) and (cell pad) same (set group) same (chip layout) same placement same (switch\$4 current)same (system program) and "716"/\$.ccls.	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/06/13 17:37
S26	48	(I/O) and (cell pad) same (set group) same (chip layout) same placement and (switch\$4 current)same (system program) and "716"/\$.ccls.	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/12/06 10:10
S27	2	(I/O adj pad) same (set group) same (chip layout) same placement and (switch\$4 current)same (system program) and "716"/\$.ccls.	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/06/13 17:39
S28	2	(I/O adj pad) same position\$4 same (set group) same (chip layout) same placement and (switch\$4 current)same (system program) and "716"/\$.ccls.	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/06/13 17:40
S29	6	(I/O adj pad) and position\$4 same (set group) same (chip layout) and placement and (switch\$4 current)same (system program) and "716"/\$.ccls.	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/06/13 17:42
S30	58	(I/O adj pad) and position\$4 and placement and (switch\$4 current)same (system program method) and "716"/\$.ccls.	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/06/13 17:44
S31	256	(I/O adj pad) same (switch\$4 current)same (system program method)	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/06/13 17:48
S32	4	(I/O adj pad) same (switch\$4 adj current)	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/06/13 17:51

S33	781	(I/O adj pad) same (current)	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/06/13 17:56
S34	42	(I/O adj pad) same (calculat\$4 comput\$4 evaluat\$4 process\$4) same placement	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/06/13 18:00
S35	38	(I/O adj pad) same (calculat\$4 comput\$4 evaluat\$4 process\$4) same placement and @ad<"20030624"	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/06/13 18:19
S36	2461	(I/O pad) same (calculat\$4 comput\$4 evaluat\$4 process\$4) same placement and @ad<"20030624"	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/06/13 18:19
S37	179	(I/O pad) same (calculat\$4 comput\$4 evaluat\$4 process\$4) same placement and "716"/\$.ccls. and @ad<"20030624"	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/06/13 18:35
S38	37	(I/O) same(pin pad) same (calculat\$4 comput\$4 evaluat\$4 process\$4) same placement and "716"/\$.ccls. and @ad<"20030624"	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/06/13 18:36
S39	83	(I/O) same(pin pad cell) same (calculat\$4 comput\$4 evaluat\$4 process\$4) same placement and "716"/\$.ccls. and @ad<"20030624"	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/06/13 18:43
S40	588	(I/O) and (pin pad cell) same placement and "716"/\$.ccls. and @ad<"20030624"	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/06/13 18:45
S41	1562	(I/O) and (pin pad cell) and "716"/\$.ccls. and @ad<"20030624"	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/06/13 18:45
S42	811	(I/O) and (pin pad cell) and placement and "716"/\$.ccls. and @ad<"20030624"	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/06/13 18:46
S44	10	(I/O) and (pin pad cell) and placement same current same switching and "716"/\$.ccls. and @ad<"20030624"	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/06/13 18:47
S45	120	(I/O) and (pin pad cell) same (placement routing)same current and "716"/\$.ccls. and @ad<"20030624"	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/06/13 18:50

S46	168	(I/O) and (pin pad cell) same (placement routing layout) same current and "716"/\$.ccls. and @ad<"20030624"	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/06/13 18:51
S47	270	(I/O) and (pin pad cell) same (placement routing layout) same position\$4 and "716"/\$.ccls. and @ad<"20030624"	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/06/13 18:57
S48	52	(I/O) same (pin pad cell) same (placement routing layout) same position\$4 and "716"/\$.ccls. and @ad<"20030624"	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/06/13 18:58
S49	2491	(I/O) same (pin pad cell) same (placement routing layout) and @ad<"20030624"	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/06/13 19:03
S50	14	S49 and "switching current"	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/06/13 19:00
S51	5	(I/O) same (placement routing layout) same (switch\$4 adj current) and @ad<"20030624"	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/06/13 19:04
S52	30	aubertine	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/06/14 13:26
S53	1	aubertine.in. and "716"/\$.ccls.	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/06/14 12:47
S55	2974	((I/O)or (input adj2 output)) same (pad or pin or cell) same switch\$3 same current	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/06/14 14:07
S56	196	((I/O)or (input adj2 output)) same (pad or pin or cell) same switch\$3 same current and (placement or layout) same (position\$3 locat\$3) and (threshold or maximum)	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/06/14 13:35
S57	8	((I/O)or (input adj2 output)) same (pad or pin or cell) same (switch\$3 adj2 current) and (placement or layout) same (position\$3 locat\$3) and (threshold or maximum)	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/06/14 13:36
S58	818	((I/O)or (input adj2 output)) same (pad or pin or cell) same switch\$3 and (placement or layout) same (position\$3 locat\$3) and (threshold or maximum)	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/06/14 13:43

S59	5	((I/O)or (input adj2 output))same (pad or pin or cell) same switch\$3 same (placement or layout) same (position\$3 locat\$3) same (threshold or maximum)	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/06/14 13:43
S60	14	((I/O)or (input adj2 output))same (pad or pin or cell) same switch\$3 same (placement or layout) same (position\$3 locat\$3) same (threshold or maximum limit)	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/06/14 13:44
S61	22	((I/O)or (input adj2 output))same (pad or pin or cell) same switch\$3 same (placement or layout) same (position\$3 locat\$3) same (threshold or maximum limit\$4)	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/06/14 14:13
S63	9	((I/O)or (input adj2 output))same (pad or pin or cell) same switch\$3 same (placement or layout) same (position\$3 locat\$3) same (threshold or maximum or limit\$4) same current	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/06/14 14:24
S64	14	aubertine.in.	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/06/14 14:16
S65	12	((I/O)or (input adj2 output))same (pad or pin or cell) same switch\$3 same (placement or layout) same (position\$3 locat\$3 plac\$4) same (threshold or maximum or limit\$4) same current	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/06/14 14:29
S68	2	"6584606".pn.	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/06/14 15:35
S69	1	"6584606".pn. and "di/dt"	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/06/14 15:35
S70	2	"20040268281"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/29 14:09
S71	2	"20040268281" and control adj file	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/29 14:09

S72	0	electromigration adj2 analysis and group adj switching adj current	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/06 10:12
S73	0	electromigration adj2 analysis and switching adj current	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/06 10:12
S74	11	electromigration adj2 analysis and switching same current	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/06 10:26
S75	2	"20040268281"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/06 10:26